

















Scientific Rationale

Galactic Cosmic Rays modulation inside the heliosphere

(region dominated by solar wind and related phenomena)



- CR astrophysics (heliospere, propagation & sources modelling)
- Space weather (space radiation environment)
- Single event effect (device demage)
- CR background in space experiments

AMS-02 (Earth orbit)



Voyager (Heliosphere boundaries)





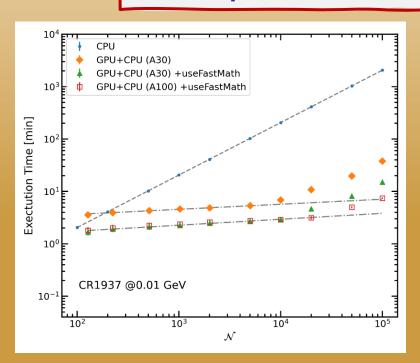


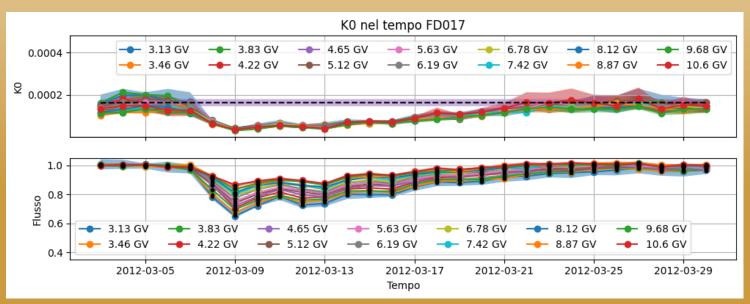




Technical Objectives

- Complete GPU porting of the SDE integration code (Cosmica)
- Code optimization based on the NVIDIA Ampere architecture (from hours to few minutes execution time)
- Implement different physical models as libraries and test physical parameters











Methodologies



$$\frac{\partial U}{\partial t} = \frac{\partial}{\partial x_i} \left(K_{ij}^S \frac{\partial U}{\partial x_j} \right) +$$

$$+\frac{1}{3}\frac{\partial V_{SW}}{\partial x_i}$$

$$\frac{\partial}{\partial T} (\alpha_{rel} TU)$$

$$\left(K_{ij}^{S}\frac{\partial U}{\partial x_{j}}\right) + \frac{1}{3}\frac{\partial V_{SW,i}}{\partial x_{i}}\frac{\partial}{\partial T}(\alpha_{rel}TU) - \frac{\partial}{\partial x_{i}}[(V_{SW,i} + V_{d,i})U]$$

Fokker-Plank (Kolmarov) differential equation



Ito's formula

$$\frac{\partial U}{\partial s} = \sum_{i} A_{B,i}(s,y) \frac{\partial U}{\partial y_i} + \frac{1}{2} \sum_{i,j} C_{B,ij}(s,y) \frac{\partial^2 U}{\partial y_i \partial y_j} - L_B U + S$$

Stochastic Differential Equations

(MC backward in time numerically integrated)



Particle propagation indipendence

SIMD parallelization on GPU

CPU-GPU mixed CUDA code



Everage of N quasi-particles simulated for each input energy

Convolved with Local Interstellar Spectrum





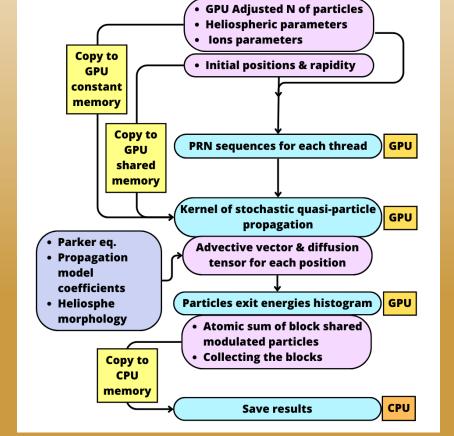
CPU





Algorithm scheme

Initialise the Simulation



Solutions

- Manage to distribute the simuated energies on all the available
 GPUs in the cluster
- Profile the code with Nsight Compute and its execution with Nsight System (bottleneck & GPU usage)
- Achieve the algorithm maximum parallelization by reducing register usage, maximising active threads
- Avoid multiple CPU GPU memory transfer
- Each warp evolves quasi-particles belonging to the same subset of heliospheric variable (maximize the probability of broadcasting)

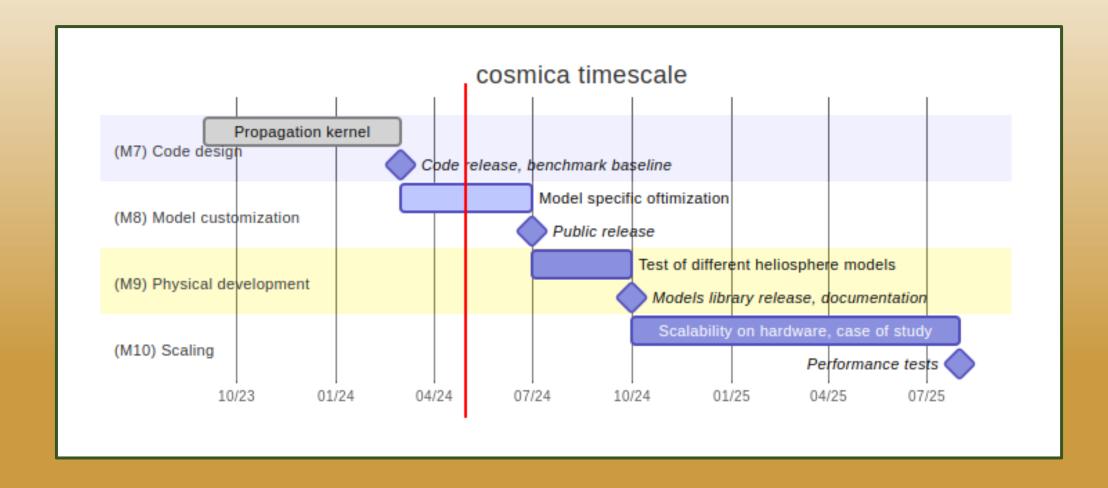








Timescale, Milestones, KPIs











From Trieste meeting (October 2023): Expected Results

Identified optimization points:

- Passing from particle energy to rigidity (one SDE becomes trivial)
- > Optimazing the registers and shared memory usage (avoid memory saturation)
- Occupancy optimization managing threads and blocks number
- > Remove the code bottleneck: branch diverging, infinity loops, stucked particle propagation
- Configure the heliosphere morphology model

Expected results:

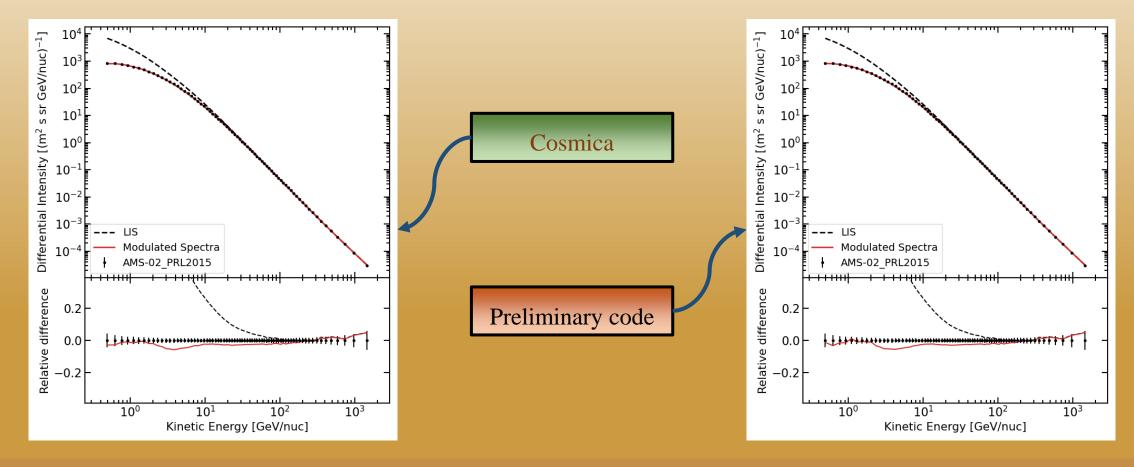
- > Produce consistent simulations with same CPU precision
- \triangleright Simulation speedup ≥ 50 times CPU code







> Stable version 1 of Cosmica (within 2% of statistical simulation error)









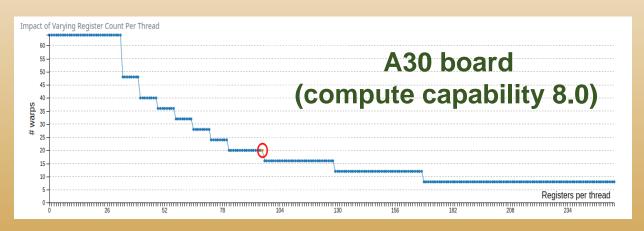


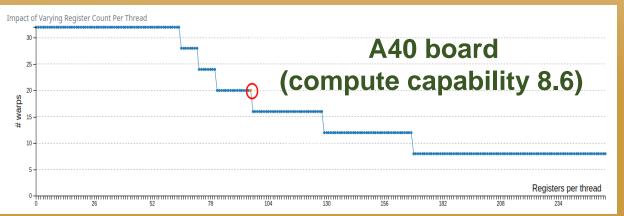
> Profiling of the execution bottleneck of single GPU kernel functions

96 registers used for the heliospheric propagation kernel function (maximum registers per thread = 32)



Registers from other warps are allocated and the number of active warps is sub-optimal













> Profiling of the execution bottleneck of single GPU kernel functions

Time 📤	Total Time	Instances	Avg	Med	Min	Max	StdDev	Category	Operation
100.0%	429.333 s	14	30.667 s	27.342 s	15.777 s	57.753 s	12.226 s	CUDA_KERNEL	HeliosphericPropagation(curandStatePhilox4_32_10 *, PropagationParameters_t, particle_t *,
0.0%	64.642 µs	18	3.591 µs	4.144 µs	608 ns	4.513 µs	1.319 µs	MEMORY_OPER	[CUDA memcpy Host-to-Device]
0.0%	55.904 µs	42	1.331 µs	1.280 µs	1.216 µs	1.792 µs	150 ns	MEMORY_OPER	[CUDA memcpy Device-to-Host]
0.0%	39.616 µs	14	2.829 µs	2.816 µs	2.816 µs	2.848 µs	16 ns	CUDA_KERNEL	kernel_max(particle_t *, float *, unsigned long)
0.0%	28.127 µs	14	2.009 µs	2.016 µs	1.984 µs	2.048 µs	22 ns	CUDA_KERNEL	histogram_atomic(const particle_t *, float, float, int, unsigned long, float *, int *)
0.0%	21.408 µs	14	1.529 µs	1.536 µs	1.504 µs	1.536 µs	13 ns	CUDA_KERNEL	histogram_accum(const float *, int, int, float *)
0.0%	6.560 µs	14	468 ns	480 ns	448 ns	480 ns	15 ns	MEMORY_OPER	[CUDA memset]
0.0%	3.008 µs	1	3.008 µs	3.008 µs	3.008 µs	3.008 µs	0 ns	CUDA_KERNEL	init_rdmgenerator(curandStatePhilox4_32_10 *, unsigned long long)

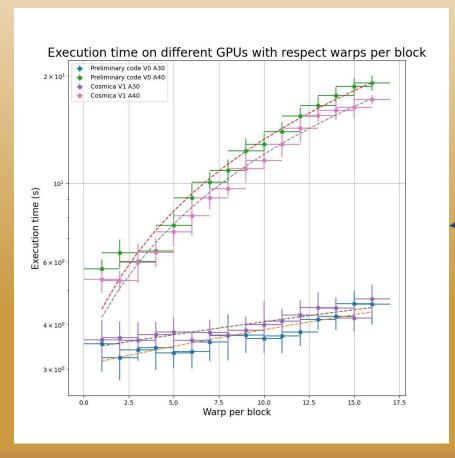
- Execution time strongly dominated by the heliospheric propagation computation
- Max exit energy search and histogram building are negligible in the execution time
- Even memory set and transfer between host and device occupy less than 0.1%







> Performance test on the local farm and different GPUs comparison



Comparable performance for A30 board

Small improvement for A40 board

NVIDIA A30

- Compute capability: 8.0
- Clock rate: 1 440 000
- Multiprocessor count: 56
- Warps per Multiprocessor: 64

NVIDIA A40

- Compute capability: 8.0
- Clock rate: 1 740 000
- Multiprocessor count: 84
- Warps per Multiprocessor: 48









> Preliminary reduction of register usage



Quasi-particle coordinate and energy are stored in shared memory

> N threads allocated and warps per block optimization

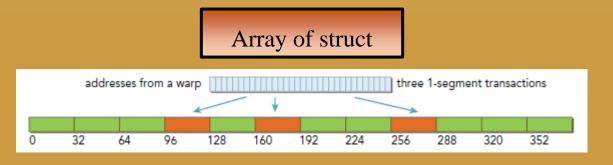


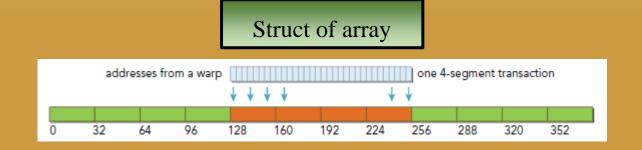
Best WpB for each GPU

N quasi-particle rounded

Full filling the first warps

> Synchronous memeory access to particle evolving variable













Computational resources used:

- **Local cluster: 10 GPUs (6 A30, 4 A40), Condor job submission system**
- > From Trieste meeting 10 k GPU hours used:
 - > Test
 - **Benchmark**
 - > Forbush calibration test

Expected computational resources comsuption:

- \triangleright Study of K_0 diffusion parameter during Forbush (application for PICA resource)
 - > At least other 10 k GPU hours
 - **▶** Memory (negligible order of GB)









Next Steps and Expected Results

NEXT STEPS:

- Passing from particle energy to rigidity (one SDE becomes trivial)
- Customization based on physical model
- > Finalize computational resource request (throught PICA)

Expected results:

> First results relevant for physical application (scale use case)











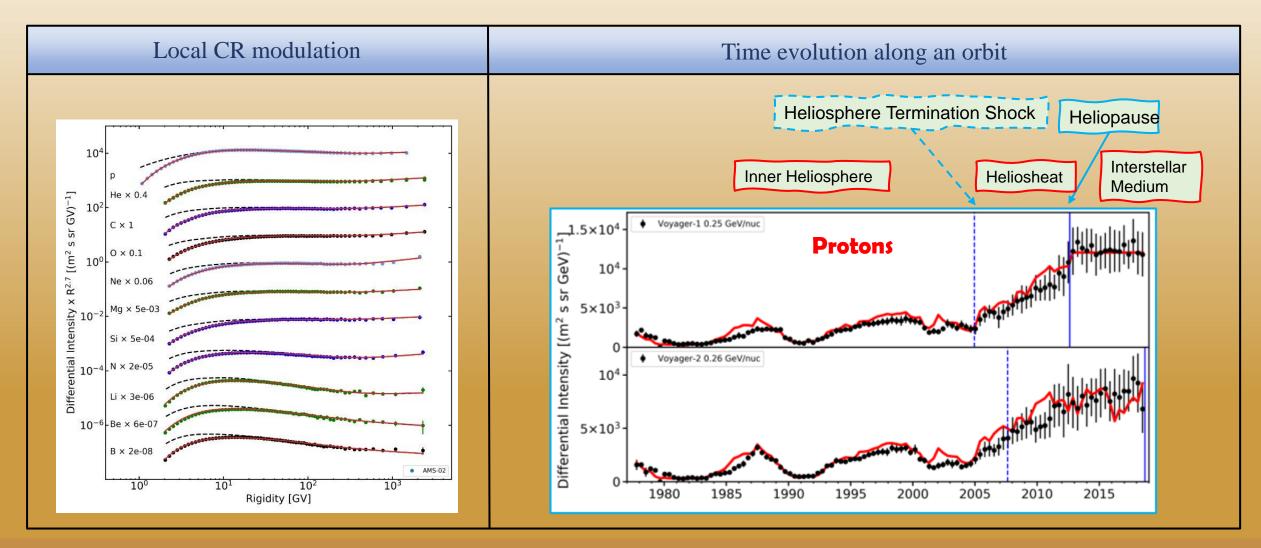








Scientific Rationale



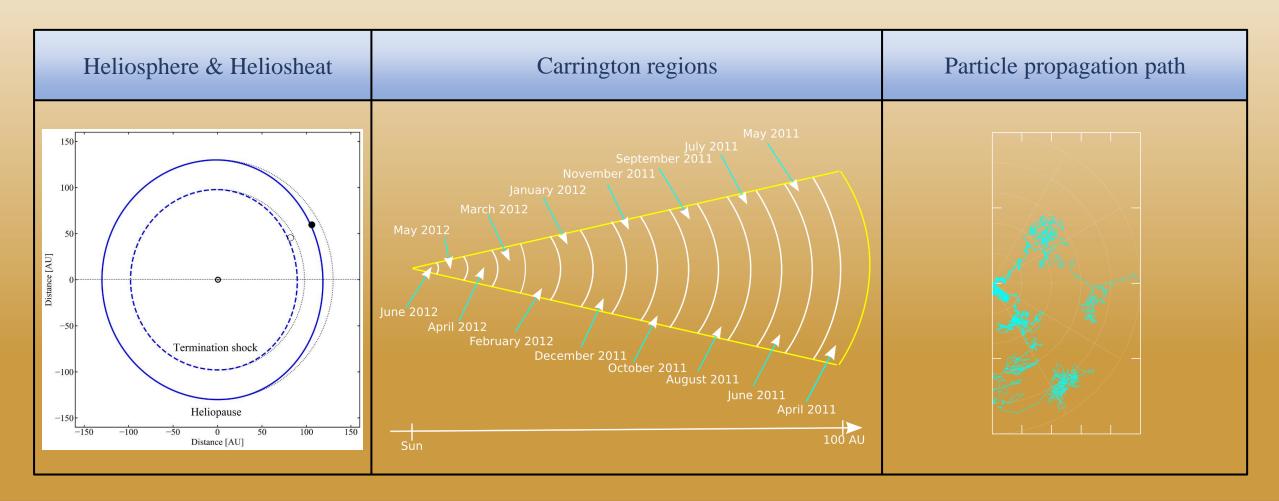








Methodologies

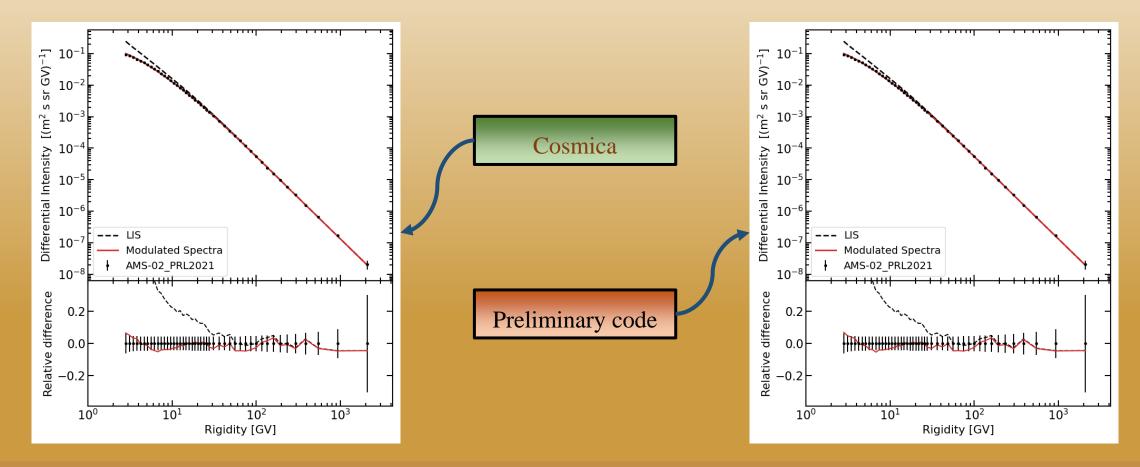








> Stable version 1 of Cosmica (within 2% of statistical simulation error)



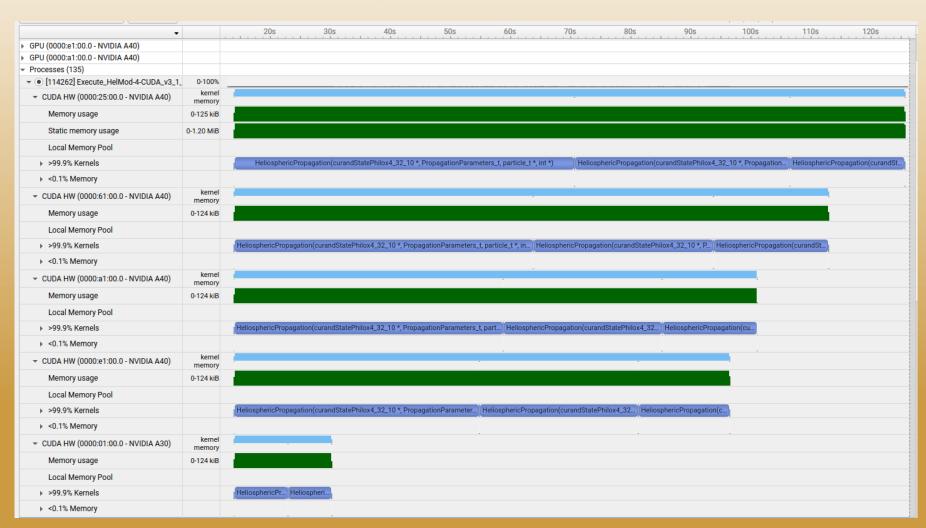






Multi GPu

- Energy bins with different avarage propagation time are not equally distributed between the GPU in the cluster
- Evident faster
 execution of the
 propagation
 computation in the
 A30 boards











• The number of warps per block was varied from 2 to 32 for each GPU board (only the avilable values of warps per block are taken into account)

Register allocation unit size	256
Register allocation granularity	warp
Max registers per Block	65536
Warp allocation granularity (for register allocation)	4
Registers used by the kernel	106

1

With more that 16 warps we exceed the GPU resources

Nregisters = 73 728 for 18 Warps

Registers allocated =

int_round \(\begin{align*} \lambda NRegPerKernel * WarpSize \\ \ RegAllocUnit * WarpGranularity \\ * RegAllocUnit * WarpGranularity * NWarps \end{align*}

We use 106 registers for the heliospheric propagation kernel function (maximum registers per thread = 32)



Registers from other warps are allocated and the number of active warps is sub-optimal

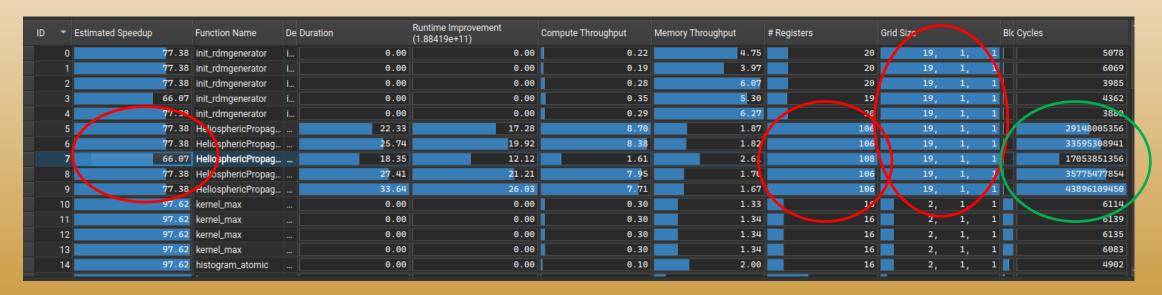








> Profiling of the execution bottleneck of single GPU kernel functions



- The grid for this launch is configured to execute only 19 blocks, which is less than the GPU's 56 multiprocessors, underutilizing some multiprocessors
- Between 66 77% improvement of the most time comsuming function



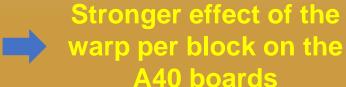




A40 multi energy



- The grid for this launch is configured to execute only 4 blocks, which is less than the GPU's 84 multiprocessors, underutilizing some multiprocessors
- Number of threads per block not a multiple of the warp dimension (rounded in the next version)
- Between 92 95% improvement of the most time comsuming











- > Stable version 1 of Cosmica
- > Profiling of the execution bottleneck of single GPU kernel functions
- > Performance test on the local farm and different GPUs comparison
- > Preliminary reduction of register usage
- > N threads allocated and warps per block optimization
- > Synchronous memeory access to particle evolving variable









Next Steps and Expected Results

Customization based on physical model:

- ➤ Insert the student thesis work (reduction of 1 model parameter)
- > Heliosheat new analysis (paper just accepted)
- > Finalize computational resource request (throught PICA)